

1 **IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

2 Application Serial No. 09/332,271
 3 Filing Date June 11, 1999
 4 Inventor Klaus Florian Schuegraf et al.
 5 Assignee Micron Technology, Inc.
 6 Group Art Unit 2811
 7 Examiner Unknown
 8 Attorney's Docket No. MI22-532
 9 Title: Methods for Forming Wordlines, Transistor Gates, and Conductive
 10 Interconnects, and Wordline, Transistor Gate, and Conductive Interconnect
 11 Structures

12 **REQUEST FOR RECONSIDERATION OF PETITION UNDER 37 CFR 1.47(a)**

13 To: Assistant Commissioner for Patents
 14 Washington, D.C. 20231

15 From: D. Brent Kenady (Tel. 509-624-4276; Fax 509-838-3424)
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19 **REMARKS**

20 The Applicant, Micron Technology, Inc., respectfully requests reconsideration
 21 of Petition Under 35 CFR 1.47(a). Examining Attorney Christina T. Tartera lists the
 22 requirements for a 35 CFR 1.47(a) petition as: (1) proof that the non-signing inventor
 23 cannot be reached or refuses to sign the oath or declaration after having been presented
 with the application papers; (2) an acceptable oath or declaration in compliance with
 35 U.S.C. §115 and §116; (3) the petition fee; and (4) a statement of the last known
 address of the non-signing inventor. The Examining Attorney Tartera states the
 Applicant's petition lacks items 1, 2 and 4.